

Senior FPGA Project and Program Manager

DRC Computer Corp - Sunnyvale (San Francisco Bay Area)

Job Description

Duties:

Project and Program management responsibility (solid reputation as leader with high standards for himself and the team).

Design of systems using PCIe bus interface with FPGA's.

Optimize designs with multiple Bus structures, DMA, and clocking, at the highest speeds.

Develop Memory controller designs and implementations with FPGA's.

Provide leadership for best practices of design, verification, and documentation methods.

The successful candidate will be responsible for FPGA designs, in current and future products and have capabilities that span the entire development process, from architectural specification through implementation, timing closure, and laboratory bring-up.

Desired Skills & Experience

Required Skills:

Self-starter that can work independently * Strong RTL design/coding experience in Verilog * Track record of shipping high-speed high-density FPGA designs in products * Substantial experience with FPGA build flow (synthesis, place-and-route, etc) * Xilinx and Altera experience (both devices and development tools) * Scripting (Perl, Tcl, Unix shell) * C/C++ * Experience with PCI Express and DDR2/3 DRAM memory interfaces * Experience with managing numerous clock domains and high-speed I/O * Module-level verification * Excellent teamwork * Excellent written and oral communication skills * Hands-on lab experience and strong debugging capability using ChipScope and SignalTap

Desired Skills:

- * A background in encryption, compression, image or video processing, or high-performance computing
- * Use of filtering and matching with FPGA's
- * GPU programming
- * Computer system architecture
- * BSEE or MSEE with at least 7-10 years of experience
- * Security Clearance and/or willingness to apply

Company Description

DRC Computer Corp is in Sunnyvale, CA

Designs and builds application acceleration technology typical implementations run up to 100X faster than X86 solutions

Additional Information

Type: Experience: Functions: Industries: Compensation:

Full-time: Senior level Engineering: FPGA Project Mgmt: Computer: Competitive+ w/bonus