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HyperTransport Consortium Announces Seven New Members

From Semiconductor to System Manufacturers, Broad Industry Support Promises On-going, Rapid Proliferation of HyperTransport Technology Adoption

SUNNYVALE, Calif., September 6, 2006 – The HyperTransport™ Technology Consortium, a non-profit organization dedicated to developing, promoting and licensing the industry's lowest latency, highest bandwidth HyperTransport interconnect technology, today counts seven new companies as part of its commercial membership base. From emerging companies with highly innovative, disruptive technologies, to established suppliers of processors and components, the new members are expected to deliver solutions that enable HyperTransport to continue its broad proliferation as a chip-to-chip, board-to-board and chassis-to-chassis interconnect technology.

New member companies include: Celoxica, Inc., DRC Computer Corp., Linux Networx, Inc., Liquid Computing Corp., NetLogic Microsystems, Inc., Supermicro Computer, Inc. and XtremeData, Inc.

"With new members spanning a wide range of market segments, the electronic industry has embraced HyperTransport as the lowest latency, highest bandwidth and most efficient interconnect technology," said Mario Cavalli, general manager of the HyperTransport Consortium. "HyperTransport-based products give OEMs a platform for delivering high-performance systems using off-the-shelf, proven and industry-standard technology. As a result, we rapidly drive down the cost and time-to-market of deploying state-of-the-art systems, which could enable scientific and technological breakthroughs never before thought possible."

New Consortium Members Fully Endorse HyperTransport

"The low latency, high bandwidth interconnect provided by HyperTransport, coupled with Celoxica's software-to-FPGA compilers and implementation technology, enables the use of FPGAs as parallel co-processors to deliver massive computing acceleration with lower power and cooling requirements," said Jeff Jussel vice president of marketing and general manager of the Americas for **Celoxica**, a leader in electronic system-level design producing software tools, FPGA acceleration boards, implementation IP and expert services that help turn software into silicon. "The development of HyperTransport as an industry standard allows Celoxica to productize FPGA-based accelerated computing solutions for use in server environments for applications in financial analysis, oil and gas exploration, and life sciences."

"True co-processing requires more than just fast logic. It requires maximum bandwidth and minimum latency. By far, the HyperTransport bus provides the industry's best interconnect," said Larry Laurich, CEO of **DRC Computer**, provider of the industry's first tightly coupled, reconfigurable processor. "Combined with DRC's RPU (Reconfigurable Processing Unit) the HyperTransport technology delivers a level of acceleration for compute intensive applications previously unattainable."

"HyperTransport provides us with the high bandwidth and low latency required to solve our customers' difficult and challenging problems," said Mark Halvorsen, senior software manager of **Linux Networx**, a company focused exclusively on Linux supercomputing and solving customers' supercomputing challenges. "The HTX interconnect standard allows us to design HyperTransport cards that can be used in off-the-shelf motherboards and therefore give us the flexibility to focus on our area of expertise. Linux Networx supports the HyperTransport Consortium because its technology is critical to our company's long term success."

"HyperTransport is a key enabler of our sustained performance scalability under load," stated Andy Church, vice president of marketing at **Liquid Computing**, the first company to deliver a new class of computer system to meet the needs of performance-driven computing users. "High performance workloads and system level enterprise virtualization required us to exploit HyperTransport to the fullest."

"Our industry-leading NETL7™ family of knowledge-based processors leverage the unique capabilities of HyperTransport interconnect technology to deliver the industry's highest performance Layer 7 application networking and network security solutions," said Mike Ichiriu, senior director of applications engineering at **NetLogic Microsystems**, the worldwide leader in the design and development of knowledge-based processors that are used for a variety of advanced Internet, corporate and other networking systems. "By allowing our Layer 7 content processors to connect directly to a CPU or packet processor, the HyperTransport interface provides high bandwidth and reduced memory access latency, allowing our customers to minimize system bottlenecks and benefit more directly from ever-increasing CPU processing power."

"As a contributing member, Supermicro Computer works closely with the HyperTransport Consortium to ensure that HyperTransport technology not only maximizes the performance of its system products but also provides end users with maximum configuration flexibility," said Tau Leng, director of Marketing and System Validation, **Supermicro Computer**, a leading manufacturer of mission-critical server and storage systems. "Our flexible motherboard designs allow our customers to benefit from the performance edge provided by the HyperTransport HTX interconnect, or to make use of conventional interconnects like PCI Express or PCI-X within the same slot location, therefore retaining the ability to upgrade to any desired level of performance and functionality at their own pace. Supermicro offers a complete range of server and motherboard products that support the HyperTransport HTX interconnect standard."

"Nearly two years ago, XtremeData recognized that high-performance computing could greatly benefit from commercial, off-the-shelf x86-based platforms coupled to FPGA co-processors. This led to our XD1000 FPGA Module, a pin-compatible drop-in replacement for an AMD Opteron - something not possible without the HyperTransport interconnect standard from the HyperTransport Consortium," said James Benbow, Chief Hardware Architect of **XtremeData**, developer of innovative solutions for high-performance computing. "The Consortium's efforts to enable and support the future roadmap of HyperTransport was a vital factor in our decision to choose HyperTransport versus other interconnect standards."

About the HyperTransport™ Technology Consortium

The HyperTransport Technology Consortium is a membership-based, non-profit organization that manages, promotes and licenses HyperTransport Technology. The HyperTransport Consortium consists of over 40 industry-leading member companies, including founding member Advanced Micro Devices, Alliance Semiconductor, Apple, Broadcom, Cisco Systems, NVIDIA, PMC-Sierra, Sun Microsystems and Transmeta. Membership is based on a yearly fee and it is open to any company interested in licensing the royalty-free use of HyperTransport technology and intellectual property. Consortium members have full access to HyperTransport technical documents database, they may attend Consortium meetings and events and may benefit from a variety of technical and marketing services, including the new, member-driven web portal, whose business benefits are part of a wide array of services offered by the Consortium free of charge to member companies. To learn more about member benefits and on how to become a Consortium member, please visit the Consortium Web site at www.hypertransport.org/consortium/cons_join.cfm. Specifications, overviews and white papers about HyperTransport technology can be found at www.hypertransport.org/tech/index.cfm.

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