



DRC Computer Wins NSF Award For FPGA-accelerated 3-D Waveform Technique

Novel Technique Will Enable New Levels of Deep Well Exploration

SUNNYVALE, CA, December 21, 2010 – DRC Computer Corporation (DRC), the leading innovator of dynamically reconfigurable coprocessors, announces that it has been awarded a National Science Foundation phase I award to develop a FPGA-accelerated 3-D Waveform Inversion technology to be used for Geophysical Exploration.

“The challenge of geophysical exploration has pushed the boundary for computational power such that new methods are being required to keep up with the complexity of deep well exploration. Having the power of accelerated 3-D waveform inversion will be essential going forward,” says Dr. Sergio Zarantonello, CEO of Algorithmica LLC, a firm specializing in computational mathematics for the earth sciences that will assist DRC on this project.

Using this new 3-D Waveform technique geophysicists will be able to more accurately survey deep oil well fields. The 3-D Waveform Inversion will be implemented on the advanced DRC Accelium™ accelerators. These devices are being used to accelerate other complex routines and algorithms providing acceleration levels of ten and one hundred times the performance of x86 CPUs. Used by many commercial, research and government organizations in a broad range of applications including energy, genomics, data security, image analysis, predictive weather analysis, cryptologics, data mining, forensics and deep packet inspection.

“DRC continues to push the boundary of accelerator based applications. This award recognizes the potential of the DRC technology in improving the cost of energy exploration,” commented Larry Laurich, DRC President, adding “In addition the very low energy consumption of the DRC Accelium accelerator coupled with very high performance provides a very green solution.”

About DRC Computer Corporation

DRC Computer Corporation is the leading innovator of dynamically reconfigurable processors, addressing the needs of time-critical, data-intense applications in the defense and finance industries, security environments, web companies, and biomedical markets. Based on the latest field programmable gate arrays (FPGAs), the DRC Accelium™ coprocessors are coded to run complex

algorithms and routines orders of magnitude faster than the same routines executing in software on an x86 processor. DRC's products provide ultra-high performance with very low energy usage (typically less than 25 watts) and minimal space requirements, producing actionable intelligence much faster (100x and more) and at significantly lower cost (90% lower) than traditional computer technologies. DRC is a wholly owned subsidiary of Security First Corp., an emerging industry leader in information assurance, data security, privacy, integrity, and high availability.

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